



PATENT

#10/10/02

In the United States Patent and Trademark Office

Applicant: SUTERA et al.

Applicant's Reference: SUNMP099

Application No: 09/430,350

Filed: October 29, 2002

For: METHOD FOR REDUCING NOISE IN
INTEGRATED CIRCUIT LAYOUTS

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) Examiner: JONES, H.

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) Art Unit: 2123

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) Date: December 3, 2002

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Technology Center 2100

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on December 3, 2002.

Signed: _____

Neely J. Welds

Separate Letter to the Official Draftsperson

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Enclosed herewith are four (4) sheets of Formal Drawings (Figs. 1, 2, 3, and 4) for the above-referenced case. If the draftsperson has any questions concerning these drawings, please contact the undersigned at the number set forth below. If any fees are due in connection with the filing of these drawings, please charge such fees to deposit account 50-0805 (Order No. SUNMP099).

Respectfully submitted,
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